



# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 1

## **Complete if Known**

Application Number 09/708,722  
 Filing Date November 9, 2000  
 First Named Inventor Stephan J. JOURDAN et al.  
 Art Unit 2183  
 Examiner Name Barry J. O'Brien  
 Attorney Docket Number 02207/9800

## **U.S. PATENT DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Document Number Number - Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US- 6,535,959	03-18-03	Ramprasad et al.	
		US- 6,427,188	07-30-02	Lyon et al.	
		US- 5,913,223	06-15-99	Sheppard et al.	
		US Publication No. 2002/0078327 A1	06-20-02	Jourdan et al.	
		US			

## **FOREIGN PATENT DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>2</sup> - Number <sup>4</sup> - Kind Code <sup>3</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	English Abstract

## **NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Bellas et al., "Architectural and Compiler Techniques for Energy Reduction in High Performance Microprocessors," <i>IEEE Transactions on VLSI</i> , Vol. 8, No. 3, June 2000, pp. 317-326.	
		Glaskowsky, Peter N., "Pentium 4 (Partially) Previewed," <i>Microprocessor Report</i> , Vol. 14, Archive 8, August 2000, p. 1, pp. 11-13.	
		Manne et al., "Pipeline Gating: Speculation Control for Energy Reduction," <i>ACM SIGARCH, Proceedings of the 23rd Annual International Symposium on Computer Architecture</i> , IEEE Computer Society Tech. Comm. on Computer Architecture, June 27-July 1, 1998, Barcelona, Spain, pp. 132-141.	
		Papworth, David B., "Tuning the Pentium Pro Microarchitecture," <i>IEEE Micro</i> , IEEE Computer Society, Vol. 16, No. 2, April 1996, pp. 8-15.	
		Solomon et al., "Micro-Operation Cache: A Power Aware Frontend for Variable Instruction Length ISA," <i>ISLPED '01</i> , August 6-7, 2001, Huntington Beach, CA, pp. 4-9.	
		Upton, Michael, "The Intel Pentium® 4 Processor," <a href="http://www.intel.com/pentium4">http://www.intel.com/pentium4</a> , October 2000, 15 pp.	

Examiner Signature	/Aimee Li/	Date Considered	05/21/2010
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kind Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 601.04. <sup>3</sup> Error Office that issued the document, by the two-letter code (WPO Standard ST 3). <sup>4</sup> For Japanese patent documents, the indication of the year of the origin of the patent must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WPO Standard ST. 10 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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